

DRIVING EVOLUTION

Automatic Code Generation For Autonomous Vehicles



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DURA AT A GLANCE





Agenda

- Requirements Modeling in SysML
- Application Code Generation Using SCADE
- SIL Testing
- Conclusion

SYSTEMS ENGINEERING & USE CASE METHODOLOGY



- Use cases are utilized for creation of System Requirements (Use Case and Supplemental)
 - SYSML Diagrams are developed and traced to use case requirements via rational model manager, the diagrams allow for a **System Architecture** to be generated.
- Systems team delivers package to the application team for development.
- System Integration Testing is performed through MIL, SIL and HIL. All test cases are stored in Rational Quality Manager and traced back to each supplemental requirement.



Systems Engineering Process



Model based systems engineering utilizes SysML following the IBM Harmony process to define the system behavior and interfaces in the IBM Jazz platform.

Example: Design A Hot Tub



Feature:

I want a hot tub that I can fill with water and then turn on. Within one day I want the temperature of the water to reach a comfortable temperature. I want to be able to set the water temperature. I want to be able to turn the hot tub off when not in use.

Credits: IBM Spa Tutorial in Rhapsody

Very simple system



Task: Control the temperature of a given amount of water with a heat source.



Defining Requirements In IBM Rhapsody: Use Cases



User story:

I want a hot tub that I can fill with water and then turn on. Within one day I want the temperature of the water to reach a comfortable temperature. I want to be able to set the water temperature. I want to be able to turn the hot tub off when not in use.

Use cases can be used to decompose

Use Cases



Contains use cases and requirements.

Also contains Rationales (e.g. results from other projects that determine a design constraint).



Defining Requirements In IBM Rhapsody



Requirements

Analysis Package

IBM stores all individual elements in packages.The actor package contains all actors.The requirements analysis package includes the use cases and the requirements.Use cases are defined graphically.



Contains use cases and requirements.

Also contains Rationales (e.g. results from other projects that determine a design constraint).



Linking Requirements To Use Cases



Relations			Tags			Properties		
General	Description	Value Properties	Flow Properties	Operations	Ports	Flow Ports	Full Ports	Proxy Ports
Arial		~ 10 ~		A	A* B	<i>I</i> <u>⊍</u> ∧ ≣	= # E	▲ ₫
This is t	he person/svs	stem that operate	s the control svs	tem. His name	e is Otto	and he's not	verv bright	

A use case analysis allows the team to focus on a limited and clearly defined portion of the feature. This increases the chances to look at all facets of the feature and to capture a larger portion of requirements.

USE IBD TO DESCRIBE THE SYSTEM



Rhapsody uses an Internal Block Diagram to describe the system.

In this case we chose a model that describes the System Under Control with one input (heat) and one output (temperature).

The other model describes the Control System with one output (heat) and one input (temperature).

Both models are black boxes in this representation but we can drill down into both models.

This is an example for a simple system architecture with only 2 blocks and 2 Interfaces.

System Architecture



After requirement analysis we are able to define a system architecture with blocks that define functionality and well defined interfaces.



Define System Context



The context is defined by the use cases, the system under control, the control system and physical constraints:

$$\Delta T = \frac{Q}{mc_p}$$

- where deltaT is the change in temperature of the water, degC
- Q is the heat input, kJ
- m is the mass of the water = 720 kg
- cp is the heat capacity of the water = 4.1855 J/g/degC

We need to transfer into the time discrete domain by using the heat output of the heater with 19 kJ/s for Q.

context



Define main elements described in the model



State Charts for the Elements

Define Behavior



State charts can be used to define the behavior of the blocks in the system architecture.



Simulating State Charts



A panel allows to visualize variables and enables user interaction.

Here we have a simple control panel on the bottom of the screen that allows us to turn the system on and off and to change the system mode. The LED show the system state and the temperature display shows the simulated water temperature. The state chart provides an overview of the logic we defined and we can either run it or set break points or step through it.

Simulate Behavior



Since we modeled the requirements we can now evaluate how the model behaves and get buy-in from stakeholders.

Simulating State Charts: SystemUnderControl





Rhapsody allows the developer to understand the state of the complete system by visualizing each sequence, state and activity diagram.

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APPLICATION SOFTWARE ENGINEERING



Process Overview

- 1. Application Team create software requirements and software design stored in **DOORS Next Generation** and traces to system requirements
- 2. Application Team uses the Internal Block Diagram generated by systems engineering to import to ANSYS SCADE
- 3. The ANSYS SCADE model is enhanced and traced to the system requirements in Doors NG
- 4. The **ANSYS SCADE** code generator produces the ISO 26262 compliant code in .c, .h, .m files, and s-functions that can be ported into a **Matlab/Simulink** s-function
- 5. The s-function is then run in SIL (e.g. ANSYS VRX or TASS Prescan) for "System Integration Tests"



Apps Software Engineering Process



The Application SW defines the SW requirements and SW architecture traced to the system architecture. The generated code is ISO26262 certified. V&V is handled through MIL and SIL and HIL.

Import IBD to SCADE









Strong Integration >>



Efficient
Traceable
Consistence

DLRA

Develop the Application Model in SCADE



Compile into 26262 compliant code



All application code is generated using an ISO 26262 complaint and qualified code generator by TUV (no additional certs needed)

Model

outC->M pre = SSM_SM1_Locked_ABC_N

outC->M_pre_ = SSM_SM1_Unselected__ABC_N;

outC->M_pre_ = SSM_SM1_WaitUnlock__ABC_N

case SSM_SM1_WaitUnlock__ABC_N : outC->foreground = black_ABC_N; outC->background = grey_ABC_N; if (inC->Unlock)

hreak:

eĺse

break;



Port Code to different targets/development tools



All generated code is portable (ANSI C, compiler, platform and OS independent, MISRA compliant)



Test Code with SIL (ex: ANSYS VRX) 🥃



cognata

CarSim

Mechanical Simulation

VIRTUAL DYNAMICS EXPLORATION

Summary

- Use cases aid requirements development
- Requirements are defined as executable SysML models
- The Internal Block Diagram can be ported to other modeling tools (SCADE)
- SCADE allows code definition through models
- Models are evaluated for FuSa
- Code is automatically MISRA compliant
- The SCADE S-Function can be integrated in MatlabSimulink
- A number of tools can be used for SIL (e.g. TASS Prescan)

Address The Challenge Of Unexpected Behavior In Modern Vehicles



THANK YOU



